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APPLICATION INFORMATION

Title Line One:: METHOD FOR TESTING SEMICONDUCTOR MEMORY
Title Line Two:: DEVICE AND TEST CIRCUIT FOR SEMICONDUCTOR
Title Line Three:: R MEMORY DEVICE
Total Drawing Sheets:: 8
Formal Drawings?:: Yes
Application Type:: Utility
Docket Number:: P/29-1642
Secrecy Order in Parent Appl.?:: No

PRIOR FOREIGN APPLICATIONS

Foreign Application One:: 2002-349275
Filing Date:: 11-29-2002

Country:: Japan
Priority Claimed:: Yes

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